

**WHAT IS CLAIMED IS:**

1. A computer executable process for adjusting traces routed through a routing area of a depiction of an electronics system, said process comprising:
  - receiving computer readable data comprising parameters defining said routing space, said traces, and obstacles within said routing space, each said trace comprising a plurality of interconnected nodes;
  - determining spacings between adjacent nodes of each trace, and adjusting a number of nodes in each trace based on said spacings;
  - assigning forces to nodes of each trace based on a proximity of said nodes to objects within said routing area, said objects including at least one of an obstacle and other nodes; and
  - moving said nodes in accordance with said forces.